Appl. No. 09/505,830

Amdt. dated March 31, 2004

Reply to Office action of December 31, 2003

## Amendments to the Specification:

Amend the paragraph beginning at page 5, line 6 as follows:

--Non-Volatile Memory circuit 20 is connected to a KVG-68

KGV-68 encryption device 24 which allows Non-Volatile Memory

circuit 20 to load a crypto key with its corresponding check word

into the encryption device 24. The encryption device is

connected to a telemeter transmitter 26 which transmits encrypted

telemetry data from an encryption device 24 to a ground

station.--

Amend the paragraph beginning at page 7, line 1 as follows:

--At this time it should be noted that the software of Appendix A is adapted for processing two KGV-68 although only one is illustrated in FIG. 1. In a security upgrade configuration the software operates in a manner which allows two KGV-68 encryption units to be loaded with a crypto key and its corresponding check word. It should be noted that while FIG. 1 only shows one KVG-68 KGV-68, the non-volatile memory comprising the present invention may be easily modified to accommodate to KVG-68 KGV-68 encryption units.

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Amend the paragraph beginning at page 7, line 22 as follows:



--The XMTR\_DISABLE output from microprocessor 32 is set high during initialization to disable transmitter 26.

The ENCR\_SENSE\_IN output from microprocessor 32 is set low during initialization indicating that the KVG-68 KGV-68 encryption device 24 is not being loaded. The ENCR\_FCLK and ENCR\_FDATA outputs from microprocessor 32 are set high during initialization. The clock signal provided by microcontroller 32 at the ENCR\_FCLK output from microcontroller 32 has an active falling edge necessitating that the signal be set high during initialization of microcontroller 32. Setting the ENCR\_FDATA output from microprocessor 32 high results in "0" at the ENCR\_FDATA output of microprocessor 32.--